# ATA6622C/24C/26C

## ATA6622C/24C/26C Errata and Data Sheet Clarifications

The functionality of the ATA6622C/24C/26C devices you have received is described in the Device Data Sheet 49860-AUTO-10/14 except for the anomalies described in this document.

There are no silicon errata issues for the ATA6622C/24C/26C. This document contains detailed information in addition to the ATA6622C/24C/26C data sheet 49860-AUTO-10/14.

There is no change in the Die itself.

## 1.0 SILICON ISSUES

None.

## 2.0 DATA SHEET CLARIFICATIONS

In the Device Data Sheet 49860-AUTO-10/14, the following clarifications and supplements should be noted:

### 2.1 Package Drawing

The package drawings have been updated. They have been converted from the former Atmel style to the Microchip Format. The only dimensions which are different are listed in the following table:

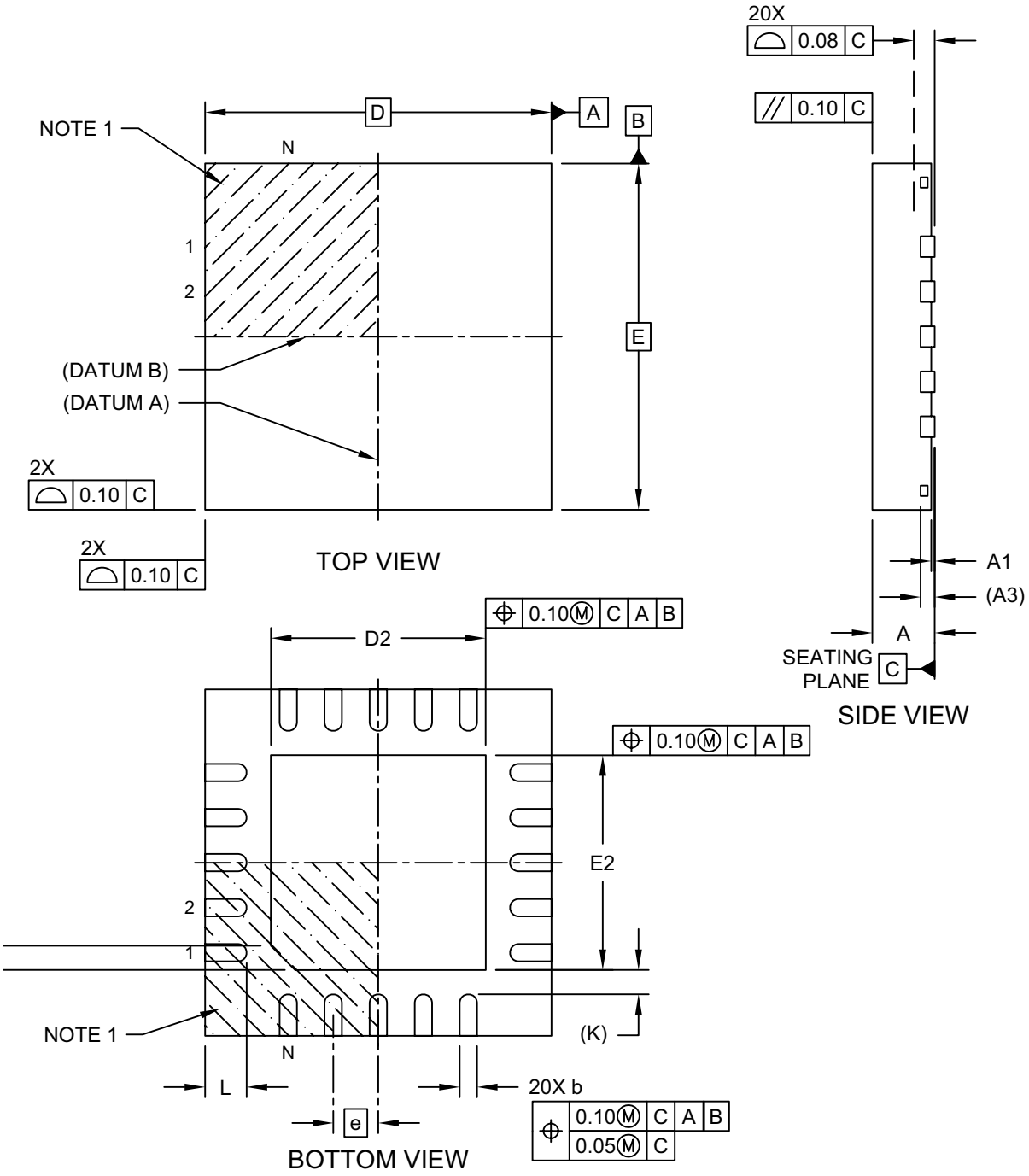
**TABLE 2-1: LIST OF UPDATED DIMENSIONS**

| | | OLD | | | NEW | | | |
|---|---|---|---|---|---|---|---|---|
| **Dimension** | **Symbol** | **Min.** | **Nom.** | **Max.** | **Min.** | **Nom.** | **Max.** | **Units** |
| Overall Height | A | 0.80 | 0.85 | 0.90 | 0.80 | 0.90 | 1.00 | mm |

The new package drawings are depicted on pages 2, 3 and 4:

**20-Lead Very Thin Plastic Quad Flat, No Lead Package (RWB) - 5x5 mm Body [VQFN]**
**With 3.1 mm Exposed Pad, 0.25 mmTerminal Width; Atmel Legacy Global Package Code ZQM**

| **Note:** | For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging |
|---|---|

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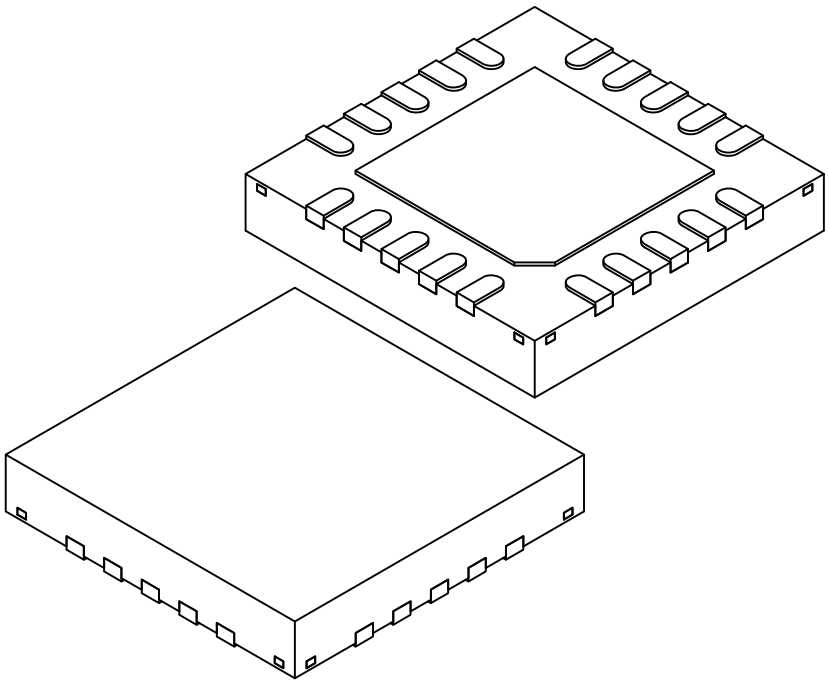
**20-Lead Very Thin Plastic Quad Flat, No Lead Package (RWB) - 5x5 mm Body [VQFN]**
**With 3.1 mm Exposed Pad, 0.25 mmTerminal Width; Atmel Legacy Global Package Code ZQM**

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging



| Units | | MILLIMETERS | | |
|---|---|---|---|---|
| Dimension Limits | | MIN | NOM | MAX |
| Number of Terminals | N | | 20 | |
| Pitch | e | | 0.65 BSC | |
| Overall Height | A | 0.80 | 0.90 | 1.00 |
| Standoff | A1 | 0.00 | 0.035 | 0.05 |
| Terminal Thickness | A3 | | 0.203 REF | |
| Overall Length | D | | 5.00 BSC | |
| Exposed Pad Length | D2 | 3.00 | 3.10 | 3.20 |
| Overall Width | E | | 5.00 BSC | |
| Exposed Pad Width | E2 | 3.00 | 3.10 | 3.20 |
| Terminal Width | b | 0.20 | 0.25 | 0.30 |
| Terminal Length | L | 0.55 | 0.60 | 0.65 |
| Terminal-to-Exposed-Pad | K | | 0.35 REF | |

Notes:

1. Pin 1 visual index feature may vary, but must be located within the hatched area.
2. Package is saw singulated
3. Dimensioning and tolerancing per ASME Y14.5M
   BSC: Basic Dimension. Theoretically exact value shown without tolerances.
   REF: Reference Dimension, usually without tolerance, for information purposes only.

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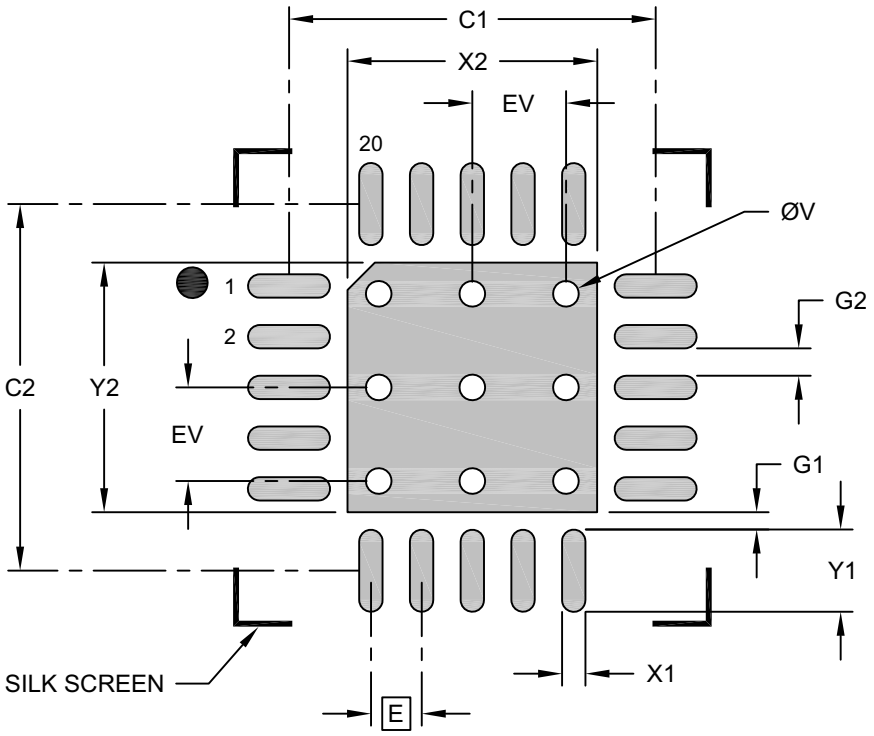
**20-Lead Very Thin Plastic Quad Flat, No Lead Package (RWB) - 5x5 mm Body [VQFN]**
**With 3.1 mm Exposed Pad, 0.25 mmTerminal Width; Atmel Legacy Global Package Code ZQM**

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging




RECOMMENDED LAND PATTERN

| Units | | MILLIMETERS | | |
|---|---|---|---|---|
| Dimension Limits | | MIN | NOM | MAX |
| Contact Pitch | E | | 0.65 BSC | |
| Optional Center Pad Width | X2 | | | 3.20 |
| Optional Center Pad Length | Y2 | | | 3.20 |
| Contact Pad Spacing | C1 | | 4.70 | |
| Contact Pad Spacing | C2 | | 4.70 | |
| Contact Pad Width (X20) | X1 | | | 0.30 |
| Contact Pad Length (X20) | Y1 | | | 1.05 |
| Contact Pad to Center Pad (X20) | G1 | 0.23 | | |
| Contact Pad to Contact Pad (X16) | G2 | 0.35 | | |
| Thermal Via Diameter | V | | 0.33 | |
| Thermal Via Pitch | EV | | 1.20 | |

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M
   BSC: Basic Dimension. Theoretically exact value shown without tolerances.
2. For best soldering results, thermal vias, if used, should be filled or tented to avoid solder loss during reflow process

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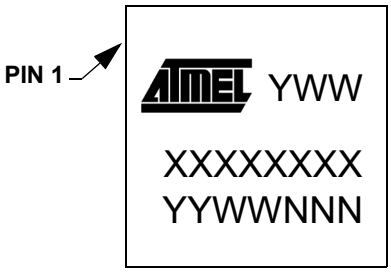
## 2.2 Package Marking Information

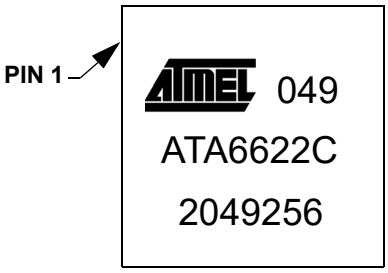
In the ATA6622C/24C/26C data sheet 49860-AUTO-10/14 there is no information regarding the Package Marking. Therefore, this information is listed here:

20-Lead VQFN (5x5 x0.9 mm)




| Part Number |
|---|
| ATA6622C |
| ATA6624C |
| ATA6626C |

Example




| | | |
|---|---|---|
| **Legend:** | XX...X | Customer-specific information |
| | Y | Year code (last digit of calendar year) |
| | YY | Year code (last 2 digits of calendar year) |
| | WW | Week code (week of January 1 is week ‘01’) |
| | NNN | Alphanumeric traceability code |
| | (e3) | Pb-free JEDEC® designator for Matte Tin (Sn) |
| | * | This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package. |

**Note**: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information.

## 2.3 Trademarks Page

The information on page 9 is also not available in the existing data sheet:

## APPENDIX A: DOCUMENT REVISION HISTORY

### Rev. A (December 2020)

- Initial Release of this Document: Errata released as a separate document. This Errata completes the Device Data Sheet 49860-AUTO-10/14 with the following information:
  - Updated Package Drawing and dimensions for the 20-Lead VQFN package.
  - Added Package Marking Information.
  - Updated Microchip Trademarks Page.

**NOTES:**

**Note the following details of the code protection feature on Microchip devices:**

- Microchip products meet the specifications contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is secure when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods being used in attempts to breach the code protection features of the Microchip devices. We believe that these methods require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Attempts to breach these code protection features, most likely, cannot be accomplished without violating Microchip's intellectual property rights.
- Microchip is willing to work with any customer who is concerned about the integrity of its code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of its code. Code protection does not mean that we are guaranteeing the product is "unbreakable." Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.



*For information regarding Microchip's Quality Management Systems, please visit www.microchip.com/quality.*

**Trademarks**



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