



Product Change Notification / SYST-28GPSJ869

Date:

16-Jun-2015

Product Category:

16-Bit - Microcontrollers and Digital Signal Controllers

PCN Type:

Silicon Die Revision

Notification Subject:

ERRATA - dsPIC33EPXXXGM3XX/6XX/7XX Family Silicon Errata and Data Sheet Clarification Silicon Die Revision Addition

Affected CPNs:

[SYST-28GPSJ869_Affected_CPN_06162015.pdf](#)

[SYST-28GPSJ869_Affected_CPN_06162015.csv](#)

Notification Text:

SYST-28GPSJ869

Microchip has released a new DeviceDoc for the dsPIC33EPXXXGM3XX/6XX/7XX Errata of devices. If you are using one of these devices please read the document located at [dsPIC33EPXXXGM3XX/6XX/7XX Errata](#).

Notification Status: Final

Description of Change: 1) Addition of silicon die revision A3. 2) Updates Table 1, Table 2 and all Affected Silicon Revision tables with new revision ID, "A3". 3) Replaces silicon issue 5. Module: "Input Capture". 4) Add silicon issues 29. Module: "PWM", 30. Module: "PWM" and 31. Module: "Input Capture". 5) Adds data sheet clarifications 3. Module: "Pin Diagrams", 4. Module: "Pin Diagrams", 5. Module: "Pin Diagrams" and 6. Module: "Electrical Characteristics".

Impacts to Data Sheet: ADC performance enhancement will be reflected in the datasheet clarification section of the errata.

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Estimated First Ship Date: 05 Sep 2015

Markings to Distinguish Revised from Unrevised Devices: Traceability Code

Attachments:

[dsPIC33EPXXXGM3XX/6XX/7XX Errata](#)

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